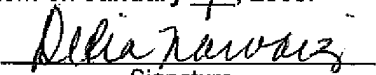
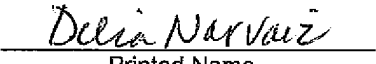


|  |                                     |
|--|-------------------------------------|
| <b>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</b>  |                                     |
| <b>REPLY TO OFFICE ACTION DATED 10/14/2008</b>   | Atty. Docket No.<br><b>SUZU1720</b> |
| Applicant<br><b>Takeshi Ishigaki</b>   |                                     |
| Application Number<br><b>10/586,951</b>  | Date Filed<br><b>07/25/2006</b>     |
| Title<br><b>Semiconductor Integrated Circuit Device and<br/>Method of Outputting Signals on Semiconductor<br/>Integrated Circuit</b> |                                     |
| Group Art Unit<br><b>2816</b>  | Examiner<br><b>Rojas, Daniel E.</b> |
| Confirmation Number:<br><b>3389</b>  |                                     |

Commissioner for Patents  
P.O. Box 1450  
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Dear Sir:

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| <b><u>Certificate of Transmission Under 37 C.F.R. § 1.8</u></b>   |
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In response to the Official Action mailed October 14, 2008, Applicant respectfully requests the Examiner reconsider the rejections of the Claims in view of this reply.